

目次

PINマッピング

ポート名順

機能毎

.....

.....

.....

1

1

7

PINマッピング

ポート名順

Port Group	Port	Signal	Pull-Up/Pull-Down	Function	Description
PA	PA0	[XIO]PA0/WKUP	Internal Pull-UP	XIO	
	PA1	ETH_MII_RX_CLK		ETHER 0	
	PA2	ETH_MDIO		ETHER 0	
	PA3	[XIO]A1		XIO	
	PA4	[XIO]SPI1_NSS/ADC/DAC1_OUT		XIO	
	PA5	[XIO]SPI1_SCK/ADC/DAC2_OUT		XIO	
	PA6	[XIO]SPI1_MISO/ADC	Internal Pull-UP	XIO	
	PA7	ETH_MII_RX_DV		ETHER 0	
	PA8	MCO1		System	PLL Clock output
	PA9	OTG_FS_VBUS		USB Device	USB Mini-B connector
	PA10	OTG_FS_ID		USB Device	USB Mini-B connector
	PA11	OTG_FS_DM		USB Device	USB Mini-B connector
	PA12	OTG_FS_DP		USB Device	USB Mini-B connector
	PA13	JTMS-SWDIO		JTAG	
	PA14	JTCK-SWCLK		JTAG	
	PA15	JTDI		JTAG	
PB	PB0	ETH_MII_RXD2		ETHER 0	
	PB1	ETH_MII_RXD3		ETHER 0	
	PB2	BOOT1		System	
	PB3	JTDO-SWO		JTAG	
	PB4	JTRST		JTAG	
	PB5	[XIO]SPI1_MOSI		XIO	
	PB6	USART1_TX		Debug CONSOLE	
	PB7	USART1_RX		Debug CONSOLE	
	PB8	[XIO]I2C1_SCL		XIO	
	PB9	[XIO]I2C1_SDA	Internal Pull-UP	XIO	
	PB10	ETH_MII_RX_ER		ETHER 0	
	PB11	ETH_MII_TX_EN		ETHER 0	
	PB12	ETH_MII_TXD0		ETHER 0	
	PB13	ETH_MII_TXD1		ETHER 0	
	PB14	[XIO]OTG_HS_DM		XIO	
	PB15	[XIO]OTG_HS_DP		XIO	

Port Group	Port	Signal	Pull-Up/Pull-Down	Function	Description
PC	PC0	[XIO]PC0		XIO	
	PC1	ETH_MDC		ETHER 0	
	PC2	ETH_MII_TXD2		ETHER 0	
	PC3	ETH_MII_TX_CLK		ETHER 0	
	PC4	ETH_MII_RXD0		ETHER 0	
	PC5	ETH_MII_RXD1		ETHER 0	
	PC6	[XIO]TIM8-1		XIO	
	PC7	[XIO]TIM8-2		XIO	
	PC8	SDIO_D0		SD CARD	
	PC9	SDIO_D1		SD CARD	
	PC10	SDIO_D2		SD CARD	
	PC11	SDIO_D3		SD CARD	
	PC12	SDIO_CK		SD CARD	
	PC13	RTC_AF1		System	
	PC14	OSC32_IN		System	
	PC15	OSC32_OUT		System	
PD	PD0	CAN1_RX		CAN	RX Data
	PD1	CAN1_TX		CAN	TX Data
	PD2	SDIO_CMD		SD CARD	
	PD3	[XIO]USART2_CTS	Internal Pull-UP	XIO	
	PD4	[XIO]USART2_RTS		XIO	
	PD5	[XIO]USART2_TX		XIO	
	PD6	[XIO]USART2_RX	Internal Pull-UP	XIO	
	PD7	[XIO]USART2_CK		XIO	
	PD8	USART3_TX		RS-232	
	PD9	USART3_RX		RS-232	
	PD10				
	PD11	USART3_CTS		RS-232	
	PD12	USART3_RTS		RS-232	
	PD13				
	PD14				
	PD15				

Port Group	Port	Signal	Pull-Up/Pull-Down	Function	Description
PE	PE0				
	PE1				
	PE2	ETH_MII_TXD3		ETHER 0	
	PE3	DIO_DO0		DOUT CH0	Photo Coupler output
	PE4	DIO_DO1		DOUT CH1	Photo Coupler output
	PE5	DIO_DO2		DOUT CH2	Photo-MOS Relay output
	PE6	DIO_DO3		DOUT CH3	Photo-MOS Relay output
	PE7				
	PE8	[XIO]TIM1_CH1N		XIO	
	PE9	[XIO]TIM1_CH1		XIO	
	PE10	MCU_LED0_G			Power LED Green (0=ON, 1=OFF)
	PE11	MCU_LED0_R			Power LED Red (0=ON, 1=OFF)
	PE12	MCU_LED1_G			Status1 LED Green (0=ON, 1=OFF)
	PE13	MCU_LED1_R			Status1 LED Red (0=ON, 1=OFF)
	PE14	MCU_LED2_G			Status2 LED Green (0=ON, 1=OFF)
	PE15	MCU_LED2_R			Status2 LED Red (0=ON, 1=OFF)

Port Group	Port	Signal	Pull-Up/Pull-Down	Function	Description
PF	PF0	I2C2_SDA		I2C Devices	M24C16, LM77, RX-8025SA
	PF1	I2C2_SCL		I2C Devices	M24C16, LM77, RX-8025SA
	PF2				
	PF3	[XIO]PF3			
	PF4	USART3_FORCEON		RS-232	RS-232 Transceiver Auto-power down mode (0=Enable, 1=Disable)
	PF5	USART3_FORCEOFF_N		RS-232	RS-232 Transceiver Power down (0=Power OFF, 1=Power ON)
	PF6	USART3_DTR		RS-232	
	PF7	USART3_DSR		RS-232	
	PF8	USART3_CD		RS-232	
	PF9	USART3_RI		RS-232	
	PF10	USART3_RI_B		RS-232	Noninverting RI (Always active)
	PF11	MCU_INITSW_N		INIT Switch	
	PF12	MCU_DIPSW0		DIP Switch[0] (0=OFF, 1=ON)	
	PF13	MCU_DIPSW1		DIP Switch[1] (0=OFF, 1=ON)	
	PF14	MCU_DIPSW2		DIP Switch[2] (0=OFF, 1=ON)	
	PF15	MCU_DIPSW3		MCU_BOOT0 (DIP Switch[3])	System memory boot (DFU) (0=Flash Boot, 1=RAM Boot)

Port Group	Port	Signal	Pull-Up/Pull-Down	Function	Description
PG	PG0				
	PG1				
	PG2				
	PG3				
	PG4				
	PG5				
	PG6	SDIO_CD_SW		SD CARD	SD SLOT Card detect
	PG7	SDIO_WP_SW		SD CARD	SD SLOT write protect switch
	PG8	SDIO_VDD_ENB			SD SLOT Power enable
	PG9	USART6_RX		RS-485	RX Data
	PG10	USART6_RCVENB_N		RS-485	Receiver Enable (0=Enable, 1=Disable)
	PG11	USART6_TCVENB		RS-485	Transceiver Enable (0=Disable, 1=Enable)
	PG12	USART6_RTS		RS-485	Reserved
	PG13	CAN1_STBY		CAN	CAN Transceiver Enable (0=Enable, 1=Disable)
	PG14	USART6_TX		RS-485	TX Data
	PG15	USART6_CTS		RS-485	Reserved

Port Group	Port	Signal	Pull-Up/Pull-Down	Function	Description
PH	PH0	OSC_IN		8MHz External CLK	
	PH1	OSC_OUT		8MHz External CLK	
	PH2	ETH_MII_CRS		ETHER 0	
	PH3	ETH_MII_COL		ETHER 0	
	PH4	ETH_RESET_N		ETHER 0	Ether PHY reset (0=Reset, 1=Normal)
	PH5	ETH_INTRP_N		ETHER 0	Ether PHY interrupt (0=Interrupt request)
	PH6	ETH_OSCENB_N		ETHER 0	Ether 25MHz OSC Enable (0=Disable, 1=Enable)
	PH7				
	PH8				
	PH9				
	PH10				Reserved (errata)
	PH11	[XIO]TIM5-2		XIO	
	PH12	[XIO]TIM5-3		XIO	
	PH13				
	PH14				
	PH15				
PI	PI0	SPI2_NSS		SPI FLASH	
	PI1	SPI2_SCK		SPI FLASH	
	PI2	SPI2_MISO		SPI FLASH	
	PI3	SPI2_MOSI		SPI FLASH	
	PI4	DIO_IN0		DIN CH0 (0=ON, 1=OFF)	
	PI5	DIO_IN1		DIN CH1 (0=ON, 1=OFF)	
	PI6	DIO_IN2		DIN CH2 (0=ON, 1=OFF)	
	PI7	DIO_IN3		DIN CH3 (0=ON, 1=OFF)	
	PI8	RTC_AF2			
	PI9	I2C2_INTB_N		I2C Devices	RTC RX-8025 Interrupt (0=Active)
	PI10				Reserved (eratta)
	PI11	I2C2_INTA_N		I2C Devices	LM77 Interrupt (0=Active)

機能毎

Function	Port	Signal Name	Mode	Direction	Pull-Up/Pull-Down	Description
8MHz External CLK	PH0	OSC_IN	CLK			8MHz External Crystal input OR 8MHz OSC input
8MHz External CLK	PH1	OSC_OUT	CLK			8MHz External Crystal output
System	PC14	OSC32_IN	CLK			32.768kHz Crystal input OR 32.768kHz OSC input
System	PC15	OSC32_OUT	CLK			32.768kHz Crystal output
System	PA8	MCO1	AF0	OUT		HSI/LSE/HSE/PLL Clock output for DEBUG
JTAG	PA13	JTMS-SWDIO	SYS	I/O	external PU	JTAG TMS (SWDIO)
JTAG	PA14	JTCK-SWCLK	SYS	IN	external PD	JTAG TCK (SWCLK)
JTAG	PA15	JTDI	SYS	IN	external PU	JTAG TDI
JTAG	PB3	JTDO-SWO	SYS	OUT	external PU	JTAG TDO (SWO)
JTAG	PB4	JTRST	SYS	IN	external PU	JTAG TRST
Debug CONSOLE	PB6	USART1_TX	AF7	OUT	external PU	CONSOLE TXD
Debug CONSOLE	PB7	USART1_RX	AF7	IN	external PU	CONSOLE RXD
RS-232	PD8	USART3_TX	AF7	OUT	external PU	RS-232 TXD
RS-232	PD9	USART3_RX	AF7	IN	external PU	RS-232 RXD
RS-232	PD11	USART3_CTS	AF7	IN	external PU	RS-232 CTS
RS-232	PD12	USART3_RTS	AF7	OUT	external PU	RS-232 RTS
RS-232	PF6	USART3_DTR	GPIO	OUT	external PU	RS-232 DTR
RS-232	PF7	USART3_DSR	GPIO	IN	external PU	RS-232 DSR
RS-232	PF8	USART3_CD	GPIO	IN	external PU	RS-232 CD
RS-232	PF9	USART3_RI	GPIO	IN	external PU	RS-232 RI
RS-232	PF10	USART3_RI_B	GPIO	IRQ IN	Internal Pull-Down	RS-232 Noninverting RI[(トランシーバパワーダウン時も常にActiveで、RI信号によるウェイクアップ等に使用可能。)]
RS-232	PF4	USART3_FORCEON	GPIO	OUT	external PD	RS-232 Transceiver Auto-power down mode[(ケーブル未接続で自動Power OFF)] (0=Enable, 1=Disable (Default Enable))
RS-232	PF5	USART3_FORCEOFF_N	GPIO	OUT	external PD	RS-232 Transceiver Power down (0=Power OFF, 1=Power ON (Default Power OFF))
RS-485	PG9	USART6_RX	AF8	IN	external PU	RS-485 RXD
RS-485	PG10	USART6_RCVENB_N	GPIO	OUT	external PU	RS-485 Receiver Enable (0=Enable, 1=Disable (Default Disable))
RS-485	PG14	USART6_TX	AF8	OUT	external PU	RS-485 TXD
RS-485	PG11	USART6_TCVENB	GPIO	OUT	external PD	RS-485 Transceiver Enable (0=Disable, 1=Enable (Default Disable))
CAN	PD0	CAN1_RX	AF9	IN	external PU	CAN RX Data
CAN	PD1	CAN1_TX	AF9	OUT	external PU	CAN TX Data
CAN	PG13	CAN1_STBY	GPIO	OUT	external PU	CAN Transceiver Enable (0=Enable, 1=Disable (Default Disable))
ETHER 0	PC1	ETH_MDC	AF11	OUT		Ether SMI Clock output
ETHER 0	PA2	ETH_MDIO	AF11	I/O	external PU	Ether SMI Data IN/OUT
ETHER 0	PA1	ETH_MII_RX_CLK	AF11	IN	Internal Pull-Down	Receive clock
ETHER 0	PA7	ETH_MII_RX_DV	AF11	IN	external PD	RX DV
ETHER 0	PC4	ETH_MII_RXD0	AF11	IN	external PU	RX data[0]

Function	Port	Signal Name	Mode	Direction	Pull-Up/Pull-Down	Description
ETHER 0	PC5	ETH_MII_RXD1	AF11	IN	external PD	RX data[1]
ETHER 0	PB0	ETH_MII_RXD2	AF11	IN	external PD	RX data[2]
ETHER 0	PB1	ETH_MII_RXD3	AF11	IN	external PU	RX data[3]
ETHER 0	PB10	ETH_MII_RX_ER	AF11	IN	external PD	RX error
ETHER 0	PH2	ETH_MII_CRS	AF11	IN	external PD	Carrier sense
ETHER 0	PH3	ETH_MII_COL	AF11	IN	external PD	Collision
ETHER 0	PC3	ETH_MII_TX_CLK	AF11	IN	Internal Pull-Down	Transmit clock
ETHER 0	PB11	ETH_MII_TX_EN	AF11	OUT	external PD	TX EN (Pull-Down for DFU Boot)
ETHER 0	PB12	ETH_MII_TXD0	AF11	OUT		TX data[0]
ETHER 0	PB13	ETH_MII_TXD1	AF11	OUT		TX data[1]
ETHER 0	PC2	ETH_MII_TXD2	AF11	OUT		TX data[2]
ETHER 0	PE2	ETH_MII_TXD3	AF11	OUT		TX data[3]
ETHER 0 (PHY)	PH4	ETH_RESET_N	GPIO	OUT	external PD	Ether PHY Reset (0=Reset ON, 1=Reset OFF (Default Reset ON))
ETHER 0 (PHY)	PH5	ETH_INTRP_N	GPIO	IRQ IN	external PU	Ether PHY interrupt request (0=Active, 1=Inactive)
ETHER 0 (PHY)	PH6	ETH_OSCENB_N	GPIO	OUT	external PU	Ether 25MHz OSC Enable (0=Disable, 1=Enable (Default Enable))
USB Device	PA9	OTG_FS_VBUS	GPIO	IN	Internal Pull-UP	Not used
USB Device	PA10	OTG_FS_ID	GPIO	IN	external PU	Not used (Pull-up for DFU boot)
USB Device	PA11	OTG_FS_DM	AF10	I/O		USB Mini-B connector / USB FS internal FS PHY Data-
USB Device	PA12	OTG_FS_DP	AF10	I/O		USB Mini-B connector / USB FS internal FS PHY Data+
SD CARD	PC8	SDIO_D0	AF12	I/O	external PU (SD+V)	SD CARD D0
SD CARD	PC9	SDIO_D1	AF12	I/O	external PU (SD+V)	SD CARD D1
SD CARD	PC10	SDIO_D2	AF12	I/O	external PU (SD+V)	SD CARD D2
SD CARD	PC11	SDIO_D3	AF12	I/O	external PU (SD+V)	SD CARD D3
SD CARD	PC12	SDIO_CK	AF12	OUT	external PU (SD+V)	SD CARD CLOCK
SD CARD	PD2	SDIO_CMD	AF12	I/O	external PU (SD+V)	SD CARD COMMAND
SD CARD	PG6	SDIO_CD_SW	GPIO	IN	external PU	SD CARD Card Detect (0=Card IN, 1=Card OUT)
SD CARD	PG7	SDIO_WP_SW	GPIO	IN	external PU	SD CARD Write Protect switch (0=Write Protect, 1=Normal)
SD CARD	PG8	SDIO_VDD_ENB	GPIO	OUT	external PD	SD CARD Power enable (0=Power OFF, 1=Power ON (Default Power OFF))
SPI FLASH	PI0	SPI2_NSS	AF5	OUT	external PU	N25Q064 Chip select
SPI FLASH	PI1	SPI2_SCK	AF5	OUT	external PD	N25Q064 Clock
SPI FLASH	PI2	SPI2_MISO	AF5	IN	external PU	N25Q064 Master IN/Serial OUT
SPI FLASH	PI3	SPI2_MOSI	AF5	OUT	external PU	N25Q064 Master OUT/Serial IN
I2C Devices	PF0	I2C2_SDA	AF4	I/O	external PU	M24C16, LM77, RX-8025SA
I2C Devices	PF1	I2C2_SCL	AF4	OUT	external PU	M24C16, LM77, RX-8025SA
I2C Devices	PI9	I2C2_INTB_N	GPIO	IRQ IN	external PU	RTC RX-8025 Interrupt request (0=Active, 1=Inactive)
I2C Devices	PI11	I2C2_INTA_N	GPIO	IRQ IN	external PU	Temperature sensor LM77 Interrupt request (0=Active, 1=Inactive)
LED	PE10	MCU_LED0_G	GPIO	OUT	external PD	Power LED Green (0=ON, 1=OFF (default ON))
LED	PE11	MCU_LED0_R	GPIO	OUT		Power LED Red (0=ON, 1=OFF)
LED	PE12	MCU_LED1_G	GPIO	OUT		Status1 LED Green (0=ON, 1=OFF)
LED	PE13	MCU_LED1_R	GPIO	OUT		Status1 LED Red (0=ON, 1=OFF)
LED	PE14	MCU_LED2_G	GPIO	OUT		Status2 LED Green (0=ON, 1=OFF)
LED	PE15	MCU_LED2_R	GPIO	OUT		Status2 LED Red (0=ON, 1=OFF)

Function	Port	Signal Name	Mode	Direction	Pull-Up/Pull-Down	Description
INIT SWITCH	PF11	MCU_INITSW_N	GPIO	IRQ IN	external PU	INIT SWITCH (0=ON, 1=OFF)
DIP SWITCH	PF12	MCU_DIPSW0	GPIO	IN	external PD	DIPSW[0] (0=OFF, 1=ON (注意：出力に設定すると壊れます))
DIP SWITCH	PF13	MCU_DIPSW1	GPIO	IN	external PD	DIPSW[1] (0=OFF, 1=ON (注意：出力に設定すると壊れます))
DIP SWITCH	PF14	MCU_DIPSW2	GPIO	IN	external PD	DIPSW[2] (0=OFF, 1=ON (注意：出力に設定すると壊れます))
MCU_BOOT0	PF15	BOOT0/MCU_DIPSW3	GPIO	IN	external PD	Boot mode[0] (0=Flash Boot, 1=RAM Boot) (boot up sequence)
DIP SWITCH						DIPSW[3] (0=OFF, 1=ON) (After boot up) (注意：出力に設定すると壊れます)
System	PB2	BOOT1	GPIO	IN	external PD	Boot mode[1] = Always 0
DIN	PI4	DIO_IN0	GPIO	IN	external PU	DIN[0] (0=ON, 1=OFF (Default OFF))
DIN	PI5	DIO_IN1	GPIO	IN	external PU	DIN[1] (0=ON, 1=OFF)
DIN	PI6	DIO_IN2	GPIO	IN	external PU	DIN[2] (0=ON, 1=OFF)
DIN	PI7	DIO_IN3	GPIO	IN	external PU	DIN[3] (0=ON, 1=OFF)
DOUT	PE3	DIO_DO0	GPIO	OUT	external PU	DOUT[0] (0=ON, 1=OFF (Default OFF))
DOUT	PE4	DIO_DO1	GPIO	OUT	external PU	DOUT[1] (0=ON, 1=OFF)
DOUT	PE5	DIO_DO2	GPIO	OUT	external PU	DOUT[2] (0=ON, 1=OFF (Photo-MOS RELAY OUT))
DOUT	PE6	DIO_DO3	GPIO	OUT	external PU	DOUT[3] (0=ON, 1=OFF (Photo-MOS RELAY OUT))
XIO	PA0	XIO_PA0_WKUP	GPIO	IN	Internal Pull-UP	XBee S2B/S2C : ON/SLEEP# status (0=Sleep mode, 1=Wake up)
XIO	PA3	XIO_PA3_ADC	GPIO	OUT		XBee S2B/S2C : DTR#/SLEEP_RQ control (0=Wake up, 1=Pin sleep request)
XIO	PA4	SPI1_NSS	AF5	OUT		XBee S2B : Not used / S2C : SPI_NSSEL
XIO	PA5	SPI1_SCK	AF5	OUT		XBee S2B : Not used / S2C : SPI_CLK
XIO	PA6	SPI1_MISO	AF5	IN	Internal Pull-UP	XBee S2B : Not used / S2C : SPI_MISO
XIO	PB5	SPI1_MOSI	AF5	OUT		XBee S2B : Not used / S2C : SPI_MOSI
XIO	PB8	I2C1_SCL	AF4	OUT	Internal Pull-UP	Not used[(XBで未使用のピンはGPIO-IN, Internal Pull-UPの設定でも可)]
XIO	PB9	I2C1_SDA	AF4	I/O	Internal Pull-UP	Not used
XIO	PB14	OTG_HS_DM	AF12	I/O	Internal Pull-UP	Not used
XIO	PB15	OTG_HS_DP	AF12	I/O	Internal Pull-UP	Not used
XIO	PC0	XIO_PC0_ADC	GPIO	IN	Internal Pull-UP	XBee S2B/S2C : Associate LED[Green] status (Default)[(XBee moduleのAssociate LED機能をOFFにしてXBee[DIO5]=InputとすることでMCUからAssociate LEDの制御が可能となるMCU[PC0]=GPIO OUT (0=ON, 1=OFF))]
XIO	PC6	XIO_PC6	GPIO	OUT		XBee S2B/S2C : RESET# control (0=Reset ON, 1=Reset OFF (Default OFF))
XIO	PC7	XIO_PC7	GPIO	OUT		XBee S2B/S2C : Power Enable (OPTION未実装) (0=OFF, 1=ON (Default ON))

Function	Port	Signal Name	Mode	Direction	Pull-Up/Pull-Down	Description
XIO	PD3	USART2_CTS	AF7	IN	Internal Pull-UP	XBee S2B/S2C : CTS# output
XIO	PD4	USART2_RTS	AF7	OUT		XBee S2B/S2C : RTS# input
XIO	PD5	USART2_TX	AF7	OUT		XBee S2B/S2C : DIN (serial rxd)
XIO	PD6	USART2_RX	AF7	IN	Internal Pull-UP	XBee S2B/S2C : DOUT (serial txd)
XIO	PD7	USART2_CK	GPIO	IN	Internal Pull-UP	Not used
XIO	PE8	XIO_PE8	GPIO	IN	Internal Pull-UP	XBee S2B/S2C : Commissioning Button status (0=ON, 1=OFF)
XIO	PE9	XIO_PE9	GPIO	IN	Internal Pull-UP	XBee S2B : Not used / S2C : SPI_ATTN#
XIO	PF3	XIO_PF3_ADC	GPIO	IN	Internal Pull-Down	XBee S2B/S2C : RSSI LED status (Default)[(XBee moduleのRSSI LED機能をOFFにし てXBee[DIO10]=InputとすることでMCUからRSSI LEDの制御が可能となるMCU[PF3]=GPIO OUT (0=OFF, 1=ON))]
XIO	PH11	XIO_PH11	GPIO	IN	Internal Pull-UP	XBee S2B/S2C : DIO11/LED1[RED] control (通常は使用しない)
XIO	PH12	XIO_PH12	GPIO	IN	Internal Pull-UP	Not used
Unused PIN	PG12	USART6_RTS	GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG15	USART6_CTS	GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PC13	RTC_AF1	GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PI8	RTC_AF2	GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PD10		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PD13		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PD14		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PD15		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PE0		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PE1		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PE7		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PF2		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG0		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG1		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG2		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG3		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG4		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PG5		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH7		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH8		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH9		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH12		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH13		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH14		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PH15		GPIO	IN	Internal Pull-Down	Not used
Unused PIN	PI10		GPIO	IN	Internal Pull-Down	Not used

~~REFNOTES~~

From:

<https://ma-tech.centurysys.jp/> - **MA-X/MA-S/MA-E/IP-K Developers' WiKi**

Permanent link:

https://ma-tech.centurysys.jp/doku.php?id=sa-1xx:pinmux_info:start

Last update: **2013/09/12 15:06**